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Kenta Umebayashi
University of Oulu

Robert H. Morelos-Zaragoza
San Jose State University, robert.morelos-zaragoza@sjsu.edu

Ryuji Kohno
Yokohama National University

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Modulation Identification and Carrier Recovery System for Adaptive Modulation in Satellite Communications

Kenta Umebayashi*, Robert H. Morelos-Zaragoza[†] and Ryuji Kohno[‡]

*Centre for Wireless Communications, University of Oulu P. O. Box 4500 FIN-90014 University of Oulu, Finland
Email: ume@ee.oulu.fi

[†]San Jose State University One Washington Square San Jose, CA 95192-0084
Email: rmorelos@email.sjsu.edu

[‡]Yokohama National University 79-5 Tokiwadai, Hodogaya, Yokohama, Kanagawa, 240-8501, Japan.
Email: kohno@ynu.ac.jp

Abstract—We introduce the modulation identification technique implementing the multimode phase locked loop (PLL) in the satellite communication using adaptive modulation scheme which is a countermeasure against the rain attenuation. In the multimode PLL, phase lock detectors (PLDs) are used for not only phase lock, but also modulation identification. We present the sub-optimized design of the PLDs for modulation identification with respect to the throughput and show the validity of sub-optimization. In addition, by the comparison between the multimode PLL and conventional scheme in ISDB-S, we present the effectivity of the multimode PLL.

I. INTRODUCTION

A modulation identification technique is a challenging topic that has been studied for military, surveillance, Software Defined Radio (SDR) and non-cooperative communications. The modulation identification has played a key role in a military communication and surveillance fields. However, in the consumer applications, the efficiency of the modulation identification techniques remains unknown, therefore, this paper focus on this problem.

Wei and Mendel presented the optimum modulation classification method based on Bayes decision theory where the criterion is the minimum error-rate of the modulation classification [1]. An objective of [1] was to develop the theoretical analysis of generic Maximum Likelihood (ML) classifier for the phase and amplitude modulation schemes. In [1] and [2], they utilized the ML or Likelihood Function (LF) for the modulation identification. They have two critical problems which are the realities of assumptions and the computational complexity. [1] assumed that the synchronization is ideal and noise power is known at the receiver, however, it is unrealistic. In [2], even though, the carrier offset was considered, huge amount of computation is necessary due to the implementation of LF functions.

Based on the above problems, we have been working on multimode Phase Locked Loop (PLL) which can identify the modulation type and recover the carrier offset. The main contributions of this study are as follows. 1) We discover a sub-optimized design of Phase Lock Detectors (PLD) which

are main part of modulation identification. The modulation identification error and non-modulation identification (non-MODID) are main causes deteriorating the throughput. Here non-MODID means that there is not the modulation identification result, namely, it is detected that all of the modulation types are unreliable. For this problem, the sub-optimization in terms of throughput is derived by minimizing the summation of the modulation identification error rate (*ER*) and non-MODID rate (*NR*). 2) We present a comparison between the multimode PLL and the conventional scheme on Integrated Services Digital Broadcasting - Satellite (ISDB-S) which is a broadcasting communication from the satellite to the earth stations on the Ku-band. This comparison shows an efficiency, i.e. the throughput, based on modulation identification regarding one of the consumer applications. There has been no study that tried to evaluate the modulation identification techniques regarding the throughput or quality of the communication.

The remainder of the paper is organized as follows. In Section II, the assumptions and system block diagram are presented. In Section III, we describe the modulation identification algorithm and in Section IV, which is a main part of this paper, we present the sub-optimized design of PLDs. Numerical results are shown in Section V, and finally, we conclude and outline the future subjects in Section VI.

II. SYSTEM DESCRIPTION

We assume the adaptive modulation on ISDB-S. Assumed Channel model, described in section V, is based on the ISDB-S standard [4] and Ku band channel model [5]-[7], where the received signal power is varied due to the rain attenuation. In this paper, we concentrate on modulation identification problem at the receiver, therefore, the modulation control in the transmitter is assumed complete. Although the receiver does not have information of which modulation type is used, the information of the available modulation types is provided. In fact, in the conventional ISDB-S system, BPSK, QPSK and 8PSK are available, and the modulation type is informed by the pilot signals corresponding to redundancy. On the other

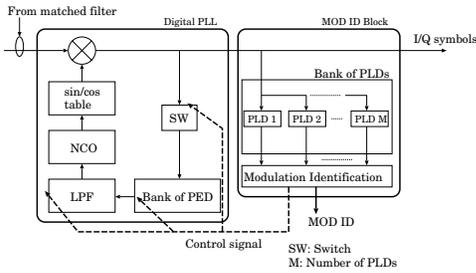


Fig. 1. System block diagram of multimode PLL

hand, in our proposal, multimode PLL has a modulation identification algorithm instead of pilot signals, hence, redundancy can be deleted. The general multimode PLL, to be hereinafter described in detail, is illustrated in Fig. 1. The n -th baseband received signal in the multimode PLL is given by

$$r(n) = \exp[-j(\phi + 2\pi\Delta f T_s n)]s(n) + g(n), \quad (1)$$

where ϕ , Δf , T_s and $g(t)$ are the initial phase offset, the carrier offset, the symbol interval and Additive White Gaussian Noise (AWGN), respectively. Let Δf be small compared with the symbol rate $R_s = 1/T_s$, thus, the received signal lies within the capture range of the digital PLL. This condition is equivalent to $\Delta f T_s \ll 1$. Equivalently, this result can also be applied to single-carrier narrow-band systems, as well. Let the *normalized frequency offset* be equal to $\Delta f T_s$. In addition, $s(n)$ is the n -th transmitted symbol baseband signal and is given by

$$s(n) = \exp[j(\psi_{MOD} + \frac{2\pi C_{MOD}(n)}{C_{MOD,MAX} + 1})] + g(n), \quad (2)$$

where ψ_{MOD} and $C_{MOD}(n)$ are number depending on the modulation type and MOD indicates the modulation type. In fact, $\psi_{BPSK} = 0$, $\psi_{QPSK} = \pi/4$ and $\psi_{8PSK} = \pi/8$, and $C_{MOD}(n)$ is integer regarding the input bits, i.e. $C_{QPSK}(n) \in (0, 1, 2, 3)$ and $C_{8PSK,MAX}(n) = 3$.

Carrier and phase offsets make it difficult to identify the modulation type. For this problem, we have implemented the conventional digital PLL which can cancel those offsets. However, several modules in multimode PLL have to be specified by the modulation type, and this can be conflicting. Namely, for the offsets problem, digital PLL can solve it, however, the modulation type must be known. On the other hand, in the modulation identification problem, as mentioned above, the phase offsets make it difficult, if we do not implement any countermeasure, e.g. digital PLL. In this study, we consider not only the modulation but also identification and phase offset problems. A solution for this problem is shown as follows by explaining the procedure and configuration of the multimode PLL. As shown in Fig. 1, there are two main functions which are modulation identification and carrier recovery. SW denotes a switch that opens or blocks the carrier recovery loop and NCO is a Numerically Controlled Oscillator outputting the signal used to cancel the offsets. As mentioned above, the Phase Error Detector (PED) and Low Pass Filter (LPF) are

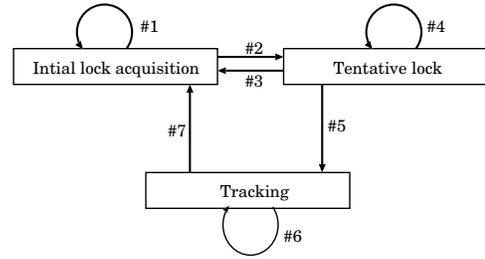


Fig. 2. State transition model of multimode PLL

designed to adapt to the transmitted modulation format. The multimode PLL has 3 states, which have key role to solve above conflicting problem. In fact, 3 states are termed 1) initial acquisition state, 2) tentative lock state and 3) tracking state as shown in Fig. 2. At the initial acquisition state, the modulation type is unknown, consequently, SW is open and the received signals are input to the modulation identification block directly without the operation of PLL. In the modulation identification block, there is a bank of PLDs prepared for each modulation type, e.g. BPSK QPSK and 8PSK PLDs. PLD outputs the lock or non-lock signal from window by window. Here, size of window is denoted as N symbols, and in this paper, $N = 50$ [3]. Once any PLD outputs lock, the modulation type can be identified, and the state transits to tentative lock state (#2 in Fig. 2). On the other hand, if lock can not be obtained by any PLD, namely this is non-MODID, the state is immovable (#1 in Fig. 2). A role of tentative lock is a reconfirmation of the modulation identification result. In the tentative lock state, PLL is reconfigured with regard to the identified modulation type in the initial acquisition state and works to cancel phase and carrier offsets with closing SW . If the modulation identification results in tentative lock state and in initial acquisition state are the same, in that case, the state is changed to tracking state (#5 in Fig. 2), otherwise, the state returns to the initial acquisition state. In fact, there is an arguable about the configuration of the state transition model, however, in this paper, we concentrate on the design of PLDs, hence, this paper presents only the procedures of the assumed state transition model. In the tracking state, PLL works in the same way as in tentative lock, and once non-lock is obtained in the PLD of the current modulation type identified in the initial acquisition and tentative lock states, the state returns to the initial acquisition state (#7 in Fig. 2).

As is evident above descriptions, an important part of the modulation identification is PLD, hence, processing and design of PLDs are described in the following section.

III. MODULATION IDENTIFICATION ALGORITHM

To explain the modulation identification algorithm, at first, the logic and design of PLDs are explained. Next, the modulation identification logic utilizing the PLD outputs is presented.

A. PLD logic and design

The PLDs can be categorized as the Basic-PLD (BPLD) and the Sub-PLD (SPLD). The main role of BPLDs is carrier

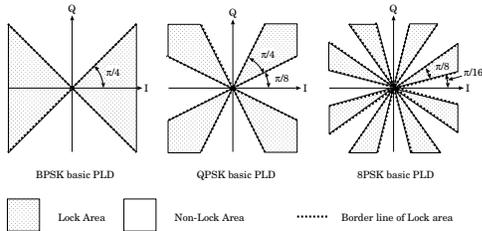


Fig. 3. Configurations of the Basic-PLDs

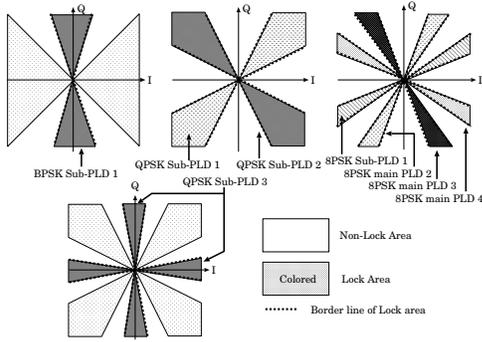


Fig. 4. Configurations of the Sub-PLDs

lock, on the other hand, the SPLDs are applied to improve the modulation identification performance. In fact, our studies have shown the performance of BPLDs in detail, and the efficiency of SPLDs. However, the theoretical analysis of the SPLDs seems to be lacking, hence, this paper intend to show the theoretical analysis and sub-optimized design of SPLD in section IV.

The BPLD of each modulation type is shown in Fig.3. Each PLD consists of the lock area and non-lock area, in addition, the design criterion is for the carrier lock detection. First motivation of this study is that the utilization of the BPLDs for the modulation identification, since the processing is similar between the lock detection and the modulation identification [3]. The behavior of BPLDs are very simple that each PLD counts the symbols lying in the lock area during a window. If the counting result $N_{C,PLD}$ is more than threshold $N_{T,PLD}$, PLD outputs 1 corresponding to lock, otherwise PLD outputs 0 corresponding to non-lock, where PLD indicates the type of PLD. In this paper, threshold of BPLDs $N_{T,PLD}$ is set at 27 according to [3]. In [3], the modulation identification algorithm employing only BPLDs was proposed and explained. However, the initial phase offset makes it difficult to identify the modulation identification, therefore, the SPLDs have been proposed. The configurations of SPLDs are shown in Fig. 4. SPLD contains the same components as in BPLD, and the behavior is also to count the symbols lying in lock area. Note the rule of lock regarding SPLDs is different from BPLD's rule. In BPSK SPLD 1 and QPSK SPLD 3 shown in Fig. 4, lock is obtained while $N_{C,PLD} < N_{T,PLD}$, on the other hand, the lock rule in other SPLDs is the same as BPLD's rule, e.g. $N_{C,PLD} > N_{T,PLD}$.

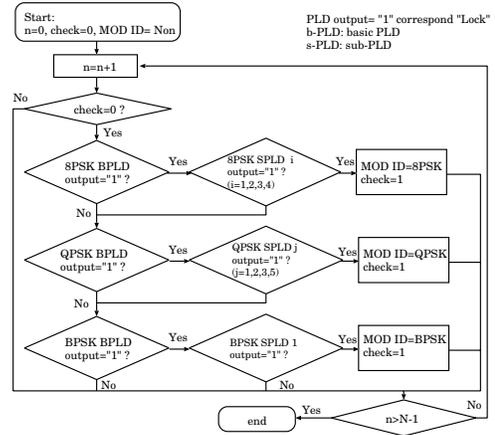


Fig. 5. Flowchart of modulation identification algorithm with Basic-PLDs and Sub-PLDs

B. Modulation identification algorithm

The detail of the modulation identification logic flowchart is shown in Fig 5. For a certain modulation type MOD , if MOD -BPLD and all of the MOD -SPLDs output lock, this modulation type MOD is the modulation identification result, and this state is denoted as "total lock". If several modulation types are candidates, e.g. "total lock" is obtained in several modulation types, the priority policy is used. In fact, the priority order is 8PSK, QPSK and BPSK as shown in Fig. 5, and this priority is based on an empirical rule [3].

IV. SUB-OPTIMIZED DESIGN OF PLD

The criteria of sub-optimization is regarding the degradation level of throughput being denoted as D_L . Before presenting the optimization problem, the criteria is examined in detail as follows. In fact, this criteria is used in the comparison between the multimode PLL and the conventional scheme on ISDB-S in section V. As presented in section I, the conventional scheme employing pilot signal which can be said redundancy, i.e. ISDB-S employs 2 symbols for pilot signal and 203 symbols for information part. On the other hand, the multimode PLL does not have any redundancy, however, the modulation identification error ER and the non-MODID NR corresponds to the degradation level of the throughput. Finally, in the conventional scheme, $D_L = \frac{2}{255}$ and the multimode PLL, $D_L = ER + NL$. ER and NR in mote carlo simulation are given by

$$ER = M_{ER}/M_{TOTAL} \quad (3)$$

$$NL = M_{NL}/M_{TOTAL} \quad (4)$$

where M_{TOTAL} , M_{ER} and M_{NL} are a number of total trials, a number of the modulation identification error and a number of the non-lock, respectively. Theoretical analysis of ER and NL are described later. Based on D_L , the optimization problem is given by

$$N_{T,SPLD,OPT} = \min_{N_{T,SPLD}} D_L, \quad (5)$$

where $N_{T,SPLD,OPT}$ is the optimized SPLD threshold. However, it is extremely difficult to obtain $N_{T,SPLD,OPT}$, therefore, we employ an alternative which is a sub-optimization in terms of minimizing D_L under the determinate conditions. In order to explain the sub-optimization problem, we have to describe the reason why the SPLD is implemented in detail first. The modulation identification error is caused by the initial phase offset and the transmitted signal bias. The later case is that for example, if the QPSK signals are generated by only $C_{QPSK}(n) = 0$ or $C_{QPSK}(n) = 2$, the signal feature is completely the same as of BPSK completely, and modulation identification error occurs. In fact, the tentative lock state is countermeasure for this problem. An advantage of this state is utilization of multiple detections, namely, not only detection in initial acquisition, but also the detections in tentative lock are utilized. Inevitably, the modulation identification error due to bias can be avoided. An example of the former problem is shown in Fig. 6 where only BPLDs are employed and the modulation type is BPSK. When $\phi = \pi/4$, most of the received signals lie in QPSK lock area, however, it is supposed that half of the received signals are in BPSK lock area. Therefore the modulation identification error occur easily. For this problem, SPLDs are employed, i.e. QPSK SPLD 1,2 and 8PSK SPLD 1,2,3,4. To explain the behavior of SPLD, we concentrate on the QPSK SPLD 1 and 2 which are a countermeasure against the modulation identification error, shown in Fig. 6, from BPSK to QPSK under initial phase offset. By implementing the QPSK SPLD1 and 2, the lock rule for QPSK is defined as shown in Fig. 5. On the other hand, the conventional lock rule, only applying the BPLDs, is shown as follows [3],

If ($N_{C,B-BPLD} > N_{T,BPLD} = 27$)
 LOCK: output 1
 otherwise
 Non-LOCK: output 0.

The additional conditions in proposed lock rule, shown in Fig. 5, $N_{C,sub-QPSK1} > N_{T,sub-QPSK1}$ & $N_{C,sub-QPSK2} > N_{T,sub-QPSK2}$, means that if there is a bias, lock rule detects the modulation type should not be QPSK. Therefore, even if the received signals modulated by BPSK have an initial phase offset, $\phi = \pi/4$, such a modulation identification error type mentioned above can be avoided, and what is a important is setting $N_{T,SPLD}$.

Optimal solution of QPSK-SPLD design can be derived from minimizing sum of the theoretical formulations of ER and NR as shown in (5). However, the derivation of ER is complicated, therefore, the total lock rate of QPSK, which is product of lock rates of QPSK BPLD and SPLD when the modulation type is BPSK in spite of ER . The sub-optimal problem can be mathematically formulated as follows:

$$N_{T,SPLD,SOPT} = \min_{N_{T,SPLD}} P_{L,Q-PLD}(B, N_{T,SPLD}) + (1 - P_{L,Q-PLD}(Q, N_{T,SPLD})), \quad (6)$$

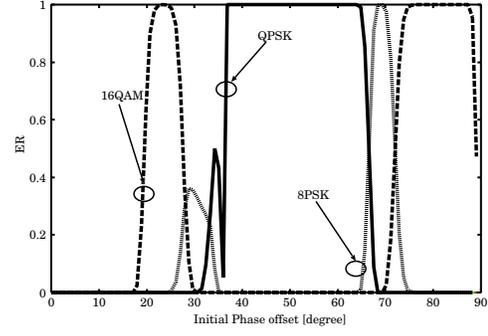


Fig. 6. Modulation Identification Error Rate vs Initial phase offset, Modulation: BPSK, $E_s/N_0=30\text{dB}$, Carrier offset=0.001, Basic-PLDs

where $P_{L,Q-PLD}$ is the total lock rate of QPSK PLDs and $0 \leq N_{T,SPLD,SOPT} \leq N$. Here, the input parameter B and Q denote the modulation type, in this case, BPSK and QPSK, respectively. In addition, $P_{L,Q-PLD}$ is given by

$$P_{L,Q-PLD} (MOD, N_{T,Q-SPLD1}) = \int_{\phi} P_{L,Q-BPLD} \sum_{l_{N2}} \sum_{l_{N1}} P_D(l_{N1}, l_{N2}) P_{L,Q-SPLD1}(N_{T,Q-SPLD1}, l_{N1}, \phi) P_{L,Q-SPLD2}(N_{T,Q-SPLD2}, l_{N2}, \phi) d\phi, \quad (7)$$

where $p_{L,PLD}$, l_{N1} and l_{N2} are lock probability of each PLD, the numbers of symbols assigned to $C_{QPSK} = 0, 2$ and $C_{QPSK} = 1, 3$, respectively. Note, the lock probability $p_{L,PLD}$ can be obtained in the same way as [3], additionally, it is essential to consider the distributions of the symbols, i.e. $P_D(l_{N1}, l_{N2})$ denoting the probability of such distribution, to obtain the total lock probability. Moreover, an approximation, which is $\Delta f = 0$, is employed in lock probability. If we consider Δf , we should consider the order of the symbols, since the each symbol locates different position. In that case, a number of cases is $N!$.

The comparison between the suboptimal solution (6) and optimal solution obtained by the monte carlo simulation is shown in Fig. 7 in terms of the QPSK and 8PSK SPLDs optimal thresholds. The difference between the suboptimal solution (6) and optimal solution is 1 at a maximum, therefore, we can achieve $N_{T,SPLD,SOPT}$ very close to $N_{T,Q-SPLD,OPT}$. Note, what we do not employ any SPLDs corresponds to $N_{T,Q-SPLD} = N$. However, in this analysis, there are 2 drawbacks. Firstly, the sub-optimal solution can not be formulated directly, namely, by calculating evaluation function (6) during $0 \leq N_{T,Q-SPSK} \leq N$, the optimal solution can be derived. Secondly, it is not proved that one or zero existence of the minimal value during $0 \leq N_{T,Q-SPSK} \leq N$. This condition can be understood intuitively, and guarantee the sub-optimal solution (6).

V. NUMERICAL EVALUATION

This paper evaluates the multimode PLL in ISDB-S system where the adaptive modulation scheme is used. At first, the channel model and assumptions regarding ISDB-S standards, next, the comparison between proposal and the conventional system is shown in terms of D_L .

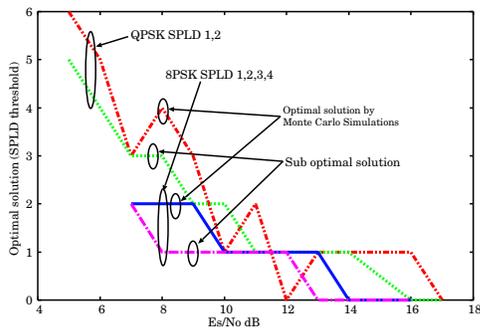


Fig. 7. Modulation Identification Error Rate vs Initial phase offset, Modulation: BPSK, Es/No=30dB, Carrier offset=0.001, Basic-PLDs

A. Channel Model

The channel model is based on ISDB-S and ITU recommendations [4]-[7]. We assume the static channel model, i.e. the rain attenuation level is fixed during a window, since the fade slope is minuscule compared with the control speed in this system. By the [5]-[7], the estimated attenuation to be exceeded for other percentages of an average year, denoted p_R , is given by

$$p_R = 10^{11.628(-0.546 + \sqrt{0.298 + 0.172 \log(0.12A_{0.001}/A_P)})} \quad (8)$$

where,

$$A_{0.001} = 0.0173(R_{0.001})^{1.205} \frac{5 - 0.075(\Phi_L - 23) - h_S}{\sin(\theta_A)(1 + L_G/(35 \exp(-0.015R_{0.001})))} \quad (9)$$

and A_P is denoted as attenuation level. $p_R(A_P)$ indicates the cumulative distribution function (CDF) of attenuation level, in addition, by the numerical calculation method the probability distribution function (PDF) can be obtained. In the computer simulations shown as follows this PDF is used to obtain the statistic performances.

TABLE I
ISDB-S PARAMETERS [4]-[7]

$R_{0.001}$	60
h_S	0km
θ_A	38.3 degree
Φ_L	35 degree
Center frequency	12GHz
Band width	34.5M Hz
Symbol rate	28.86 M symbol/sec
Frame length	205 symbols
Number of pilot signals	2 symbols
Threshold of MOD change	8dB + margin and 11dB + margin

B. Degradation Level of Throughput

Fig. 8 shows the degradation level of throughput D_L regarding carrier offset. It can be seen that the multimode PLL can decrease D_L while the margin level is more than 0dB. ISDB-S has about 7dB margin generally. Moreover, in the region where the carrier offset is less than 10^{-3} , the influence due to the carrier offset can be cancelled.

In fact, the contribution to throughput of the multimode PLL is not significant, even the simplified technique is employed.

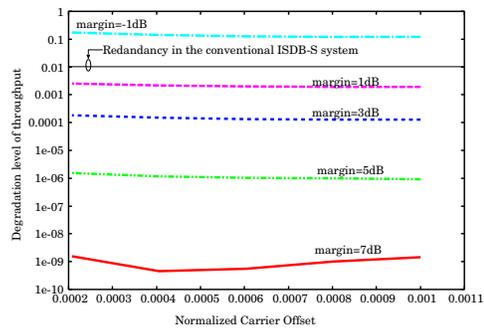


Fig. 8. D_L vs carrier offset in ISDB-S employing adaptive modulation

However, it is confirmed that the modulation identification technique has a equivalent performance to the pilot signals in the conventional scheme. In addition, for more flexible communications, the modulation identification techniques are useful.

VI. CONCLUSION AND FUTURE SUBJECTS

Multimode PLL, which can identify the modulation type and recover the carrier offset, is investigated in this paper. In particular, the main contributions are the derivation of the sub-optimal solutions of the SPLD and the evaluation of the multimode PLL on ISDB-S. Suboptimal solution is regarding to minimize D_L , in addition, a comparison of suboptimal solution and optimal solution presents the validity of suboptimal solution. Note, optimal solution is obtained by the monte carlo simulations. In the latter contribution, the multimode PLL is evaluated in terms of the throughput by comparing with the conventional scheme. It is confirmed that the multimode PLL has more advantage than conventional scheme. In addition, this paper is the first paper presenting the evaluations and efficiency of the modulation identification regarding throughput characteristic.

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