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Dojun Rhee  
*LSI Logic Corporation*

Robert H. Morelos-Zaragoza  
*SONY Computer Science Laboratories, Inc., robert.morelos-zaragoza@sjsu.edu*

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# On the Error Performance of 8-VSB TCM Decoder for ATSC Terrestrial Broadcasting of Digital Television

Dojun Rhee<sup>1</sup>  
LSI Logic Corporation  
e-mail: drhee@lsil.com

Robert H. Morelos-Zaragoza  
SONY Computer Science Laboratories, Inc.  
e-mail: morelos@cs1.sony.co.jp

**Abstract** — The error performance of various 8-VSB TCM decoders for reception of terrestrial digital television is analyzed. In previous work, 8-state TCM decoders were proposed and implemented for terrestrial broadcasting of digital television. In this paper, the performance of a 16-state TCM decoder is analyzed and simulated. It is shown that not only a 16-state TCM decoder outperforms one with 8-states, but it also has much smaller error coefficients.

## I. INTRODUCTION

The Digital Television standard [1,2] describes a broadcasting system designed to transmit high quality video and audio as well as data over a single 6 MHz channel. In order to maximize service area, the terrestrial broadcast mode incorporate both an NTSC rejection filter (in the receiver) and trellis coding. When the NTSC rejection filter is activated in the receiver, a trellis decoder for the combination of a four-state trellis encoder and the filter is used. In the paper, the error performance of various TCM decoders is studied, with and without the NTSC rejection (1-D) filter. In the previous results [1,2], a combined 8-state trellis decoder is employed for the case with NTSC rejection filter. We propose a 16-state TCM decoder and analyze and simulate its error performance. The results show that the error performance improves, with respect to 8-state TCM decoders, at the cost of doubling the memory requirements. In return, a 16-state TCM decoder has much smaller error coefficients and does not require precoding to operate.

## II. ENCODER MODEL FOR ATSC TERRESTRIAL BROADCASTING OF DIGITAL TELEVISION

In the ATSC terrestrial broadcasting system specification, the 8-VSB transmission subsystem employs a rate-2/3 4-state Ungerboeck trellis code, with the uncoded bit precoded. The 4-state feedback encoder and the bits-to-8 PAM symbol mapper are shown in Fig. 1 (a). The NTSC interference rejection (comb) filter is a one tap linear-feed-forward (1-D) filter. Its purpose is to reduce the analog NTSC interference that is caused by a carrier tone. However, the received signals are also modified. The 8 signal levels are converted to 15 levels. While providing needed co-channel interference benefits, it is well-known that the (1-D) filter degrades white noise performance by 3 dB. This is because the filter output is the subtraction of two full gain paths and, as white noise is uncorrelated from symbol to symbol, the noise power doubles. There is an additional 0.3 dB degradation due to error propagation introduced by precoding.

## III. APPROXIMATED ERROR PERFORMANCE ANALYSIS

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In the approximations presented in this section, we interpret the trellis code with decoding depth  $k$ , as a terminated zero-tail (ZT)  $(3k, 2k - m)$  block code [3]. (For the four-state trellis decoder,  $m = 2$ , while for 8- and 16-state trellis decoders,  $m = 3$  and  $m = 4$ , respectively.) When plotting the expressions with respect to the energy per bit-to-noise ratio  $(E_b/N_0)$ , a rate correction of  $R_L$  (dB) is applied, to account for the rate loss due to trellis termination, where  $R_L = (2k - m)/2k$ . With the NTSC interference rejection filter in the receiver, the number of states in the decoder increases, due to a signal constellation of increased dimensionality. In the guide to the ATSC system [2], a (1-D) filter is recommended that increases the number of signal levels from 8 of the original 8-PAM constellation to 15 at the output of the filter. To analyze the performance of the eight-state trellis decoder, a truncated union bound is computed using the technique of [3] as follows. A polynomial state transition matrix  $\Pi X$  for the 8-state trellis is used, with branch weights equal to  $X^d$ , where  $d$  denotes the squared Euclidean distance (SED) with respect to the all-zero branch and  $X$  is an indeterminant. For each set of three parallel branches between two states  $i, j$ , an element  $\pi_{i,j}(X)$  in matrix  $\Pi$  is a polynomial  $\pi_{i,j}(X) = X^{d_1} + X^{d_2} + X^{d_3}$ , where  $d_j, j = 1, 2, 3$ , denotes the SED from the branch output to the all-zero sequence output. Using symbolic mathematical software, the value of the  $k$ -th power  $\Pi^k(X)$  is computed and the coefficients of  $\pi_{0,0}^{(k)}(X)$  yield the weight distribution of the ZT  $(3k, (2k - 3))$  block code. For the 8-state trellis decoder, with  $k = 54$ ,  $\pi_{0,0}^{(54)}(X) = 5696X^{56} + 1520X^{48} + 404X^{40}$  and for 16-state trellis decoder,  $\pi_{0,0}^{(54)}(X) = 840X^{56} + 248X^{48} + 101X^{40}$ . As shown above, the error coefficients for the MSED for the 16-state trellis decoder is much smaller than that of 8-state trellis decoder.

## IV. CONCLUSION

The simulation and approximated error performance shows that a TCM decoder with 16-states outperforms one with 8-state by approximately 0.33 dB at a BER of  $10^{-5}$ . Finally, while the 8-state decoder must use a precoder for the uncoded bit to be able to decode properly, the proposed 16-state decoder does not and has a better error performance.

## REFERENCES

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